

101725,593

Form PTO 1449  U.S. Department of Commerce Patent and Trademark Office  Information Disclosure Statement by Applicant	ATTY. DOCKET NUMBER HITA.0467	SERIAL NUMBER To be Assigned
	APPLICANT SATO et al.	
	FILING DATE Concurrently Herewith	GROUP

## U.S. Patent Documents

Examiner Initial	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE
MS	2002/0097212 A1	7/25/2002	Miyazawa et al.			11-19-2001

## Foreign Patent Documents

Examiner Initial	DOCUMENT NUMBER	FILING DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION	
						YES	NO
MS	2002-251174	11/21/2001	Japan			Abstract	X

## Other Documents (Including Author, Title, Date Pertinent Pages, Etc.)

MS	Hiroshi Haga, Hiroshi Tsuchi, Katsumi Abe, Naoyasu Ikeda, Hideki Asada, Hiroshi Hayama, Kunihiro Shiota and Naruaki Takada, "A Parallel Digital-Data-Driver Architecture for Low-Power Poly-Si TFT-LCDs", SID 02 Digest (2002), pp. 690-693
EXAMINER	/Mansour Said/
	DATE CONSIDERED 05/23/2006
EXAMINER: Initial if citation is considered, whether or not citation is in conformance with MPEP 609; draw a line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant	